

CIC-56SS-44D-A6-TI

56-Lead SSOP to a 44-Pin DIP

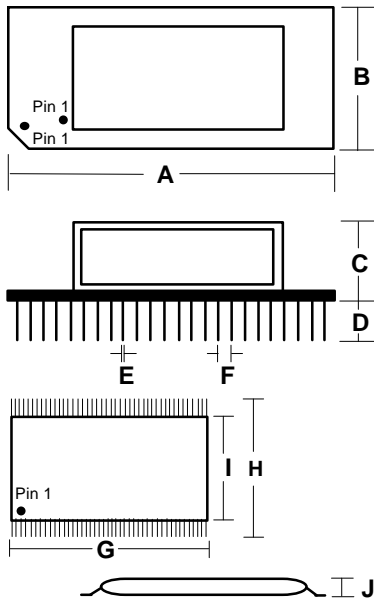
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SSOP DEVICES:	(EXAMPLES)
DA28F016SA	
DA28F016SV	(DT extended temp range)

SOCKET: Texas Instruments CSP056-054

<u>PRICING:</u>	<u>QTY</u>	<u>PRICE EACH</u>
	1-4	\$185.00
	5-9	\$166.50
	10-24	\$157.25
	25-49	\$138.75

TYPICAL DIMENSIONS: (Drawings not to scale)



DIMENSION	MM	INCH
A	62.23	2.450
B	33.02	1.300
C	17.78	0.700
D Pin Length	10.16	0.400
E Pin Diameter	0.64	0.025
F Pin Spacing	2.54	0.100
ROW SPACING	15.24	0.600
G	23.70	0.933
H	16.00	0.630
I	13.30	0.524
J	1.80	0.071
PITCH	0.80	0.031

PIN CONFIGURATION:

BASE 40-Pin DIP	PIN NAMES	SOCKET 56-Lead SSOP
1	A ₁₉	10
2	A ₁₈	11
3	A ₁₇	12
4	A ₁₆	13
5	A ₁₅	5
6	A ₁₄	4
7	A ₁₃	3
8	A ₁₂	2
9	V _{pp}	56
10	CE ₀ #, CE ₁ # ¹	1, 7
11	A ₁₁	54
12	A ₁₀	53
13	A ₉	52
14	A ₈	43
15	A ₇	45
16	A ₆	46
17	A ₅	47
18	A ₄	48
19	A ₃	49
20	A ₂	50
21	A ₁	51
22	GND (Planes 2 & 5)	15, 29, 44
23	DQ ₀	38
24	DQ ₈	39
25	DQ ₁	40
26	DQ ₉	41
27	DQ ₂	33
28	DQ ₁₀	32
29	DQ ₃	31
30	DQ ₁₁	30
31	DQ ₄	27
32	DQ ₁₂	26
33	DQ ₅	25
34	DQ ₁₃	24
35	GND (Planes 2 & 5)	15, 29, 44
36	DQ ₆	16
37	DQ ₁₄	17
38	DQ ₇	18
39	DQ ₁₅	19
40	OE#	21
41	WE#	22
42	V _{CC} (Planes 3 & 4)	14, 28, 42
43	A ₂₀	9
44 or V _{CC} Planes	WP# ²	23
GND Planes	3/5#	6
V _{CC} Planes	RP#	55
V _{CC} Planes	BYTE#	36

NOTES:

- CE₀# (pin 1) & CE₁# (pin 7) are tied together on PC board.
 - WP# (pin 23) can be jumpered to DIP Pin 44 or V_{CC} Plane of PC board
- RY/BY# (pin 20), A₀ (pin 37) & NC (pins 8,34,35) are not connected.
 - Bypass capacitors (Type 1206) may be added between V_{CC} planes & GND planes; as required by Programmer Vendor

CALIFORNIA INTEGRATION COORDINATORS, INC.

(530) 626-6168
FAX: 626-7740

656 MAIN STREET
PLACERVILLE, CA 95667

E-mail: cic@cic-inc.com
<http://www.cic-inc.com>